

REV: 1.3

SHEET

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SHEET

TITLE

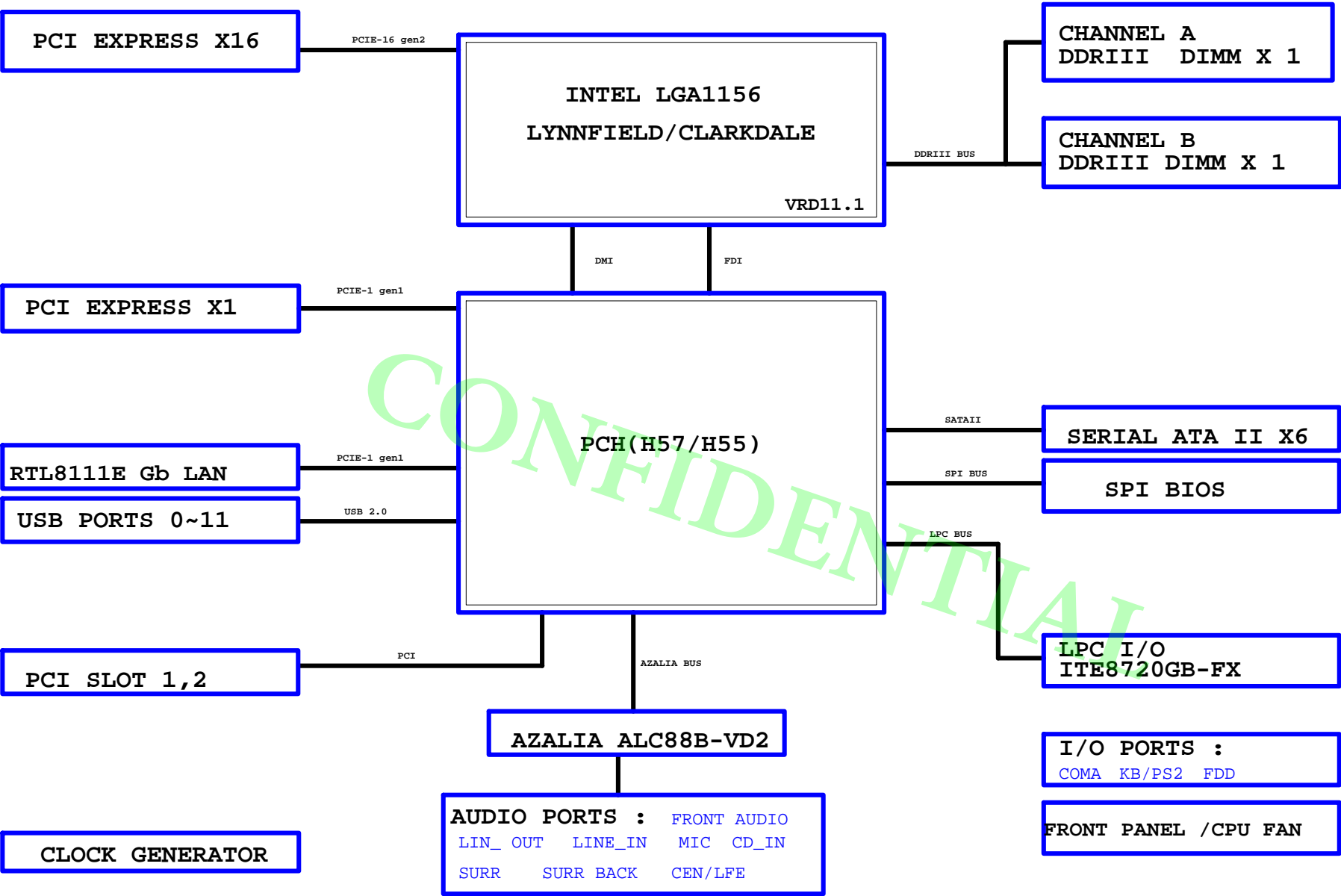
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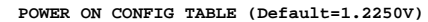
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Cover Sheet

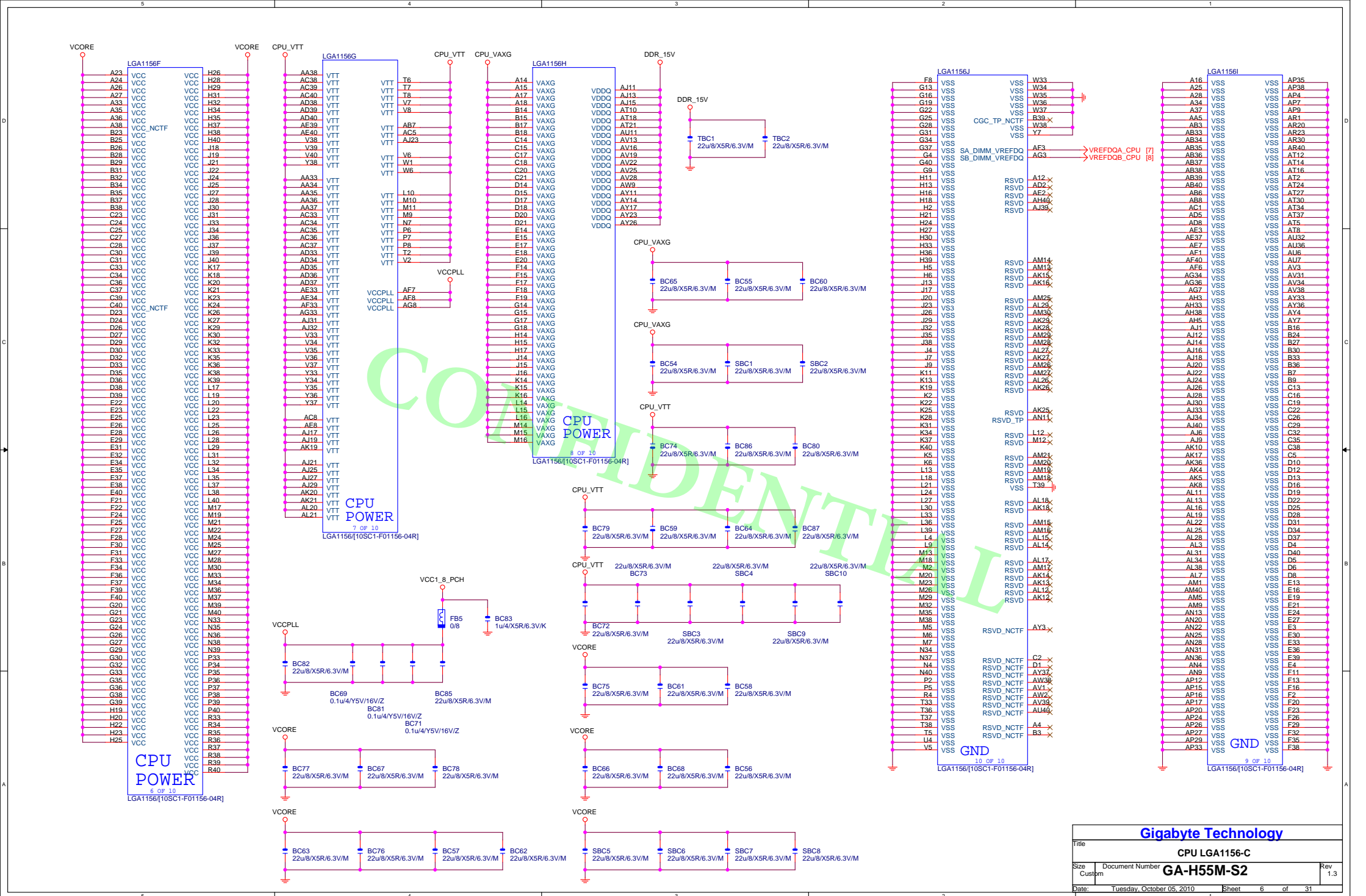
Size Custom	Document Number GA-H55M-S2	Rev 1.3
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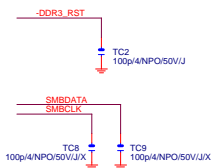
BLOCK DIAGRAM

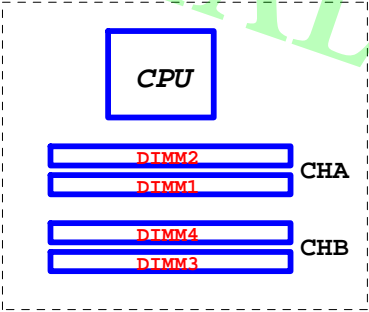
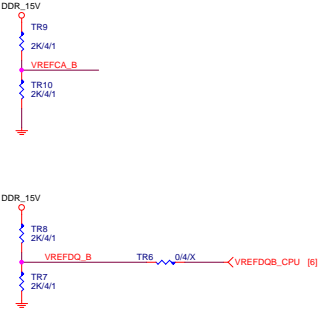
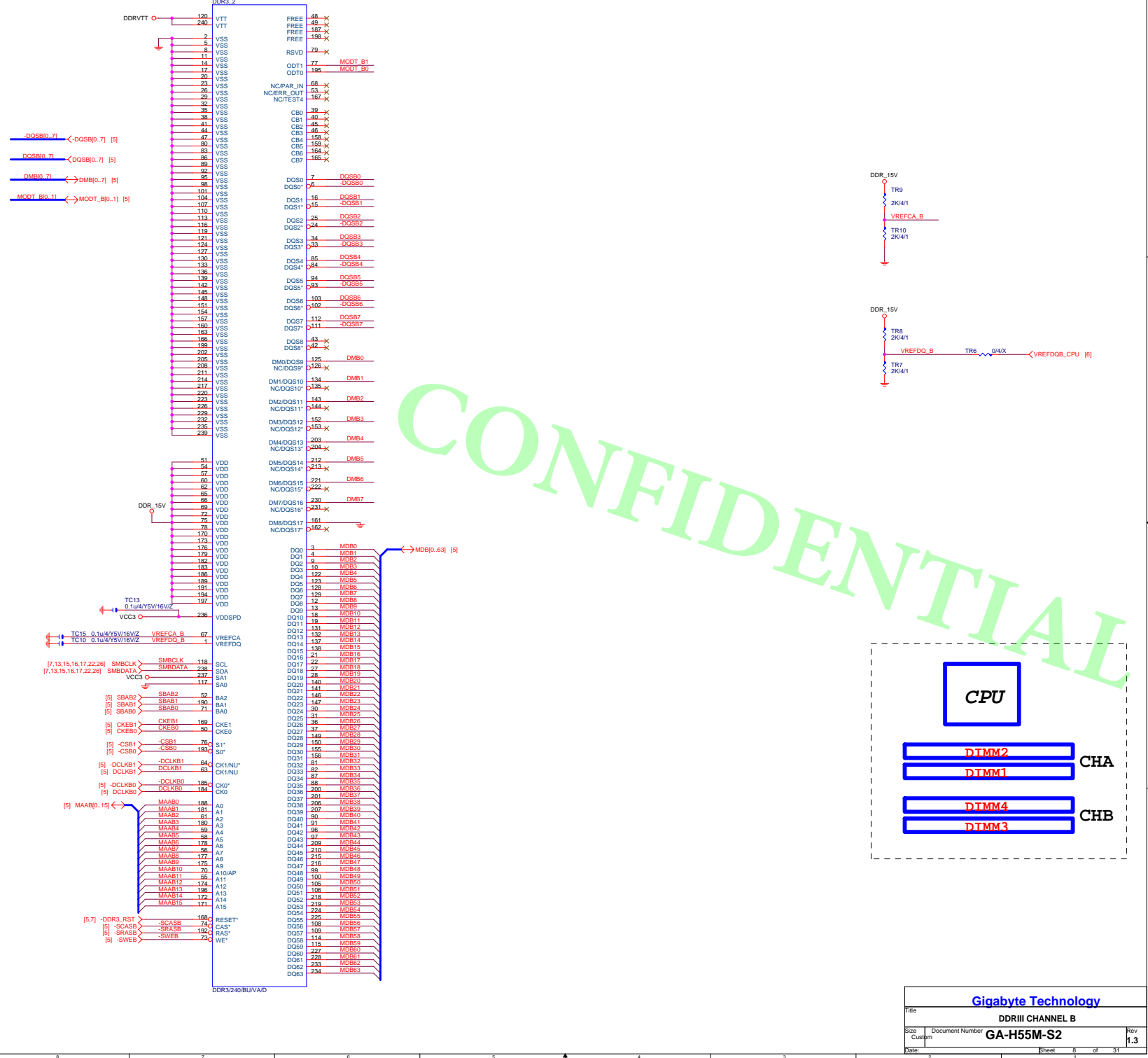




LGA1156A									
	MAAA0	AW18	SA_MA[0]	SA_DS[0]	AK3	DQSA0			
	MAAA1	AY15	SA_MA[1]	SA_DS[0]	AJ3	-DQSA0			
	MAAA2	AV15	SA_MA[2]	SA_DS[0]	AJ2	DMA0			
	MAAA3	AU15	SA_MA[3]	SA_DM[0]					
	MAAA4	AW14	SA_MA[4]	SA_DS[0]	AH1	MDA0			
	MAAA5	AY13	SA_MA[5]	SA_DS[1]	AJ4	MDA1			
	MAAA6	AV14	SA_MA[6]	SA_DS[2]	AL2	MDA2			
	MAAA7	AW13	SA_MA[7]	SA_DS[3]	AL1	MDA3			
	MAAA8	AU14	SA_MA[8]	SA_DS[4]	AG2	MDA4			
	MAAA9	AW12	SA_MA[9]	SA_DS[5]	AH2	MDA5			
	MAAA10	AT19	SA_MA[10]	SA_DS[6]	AK1	MDA6			
	MAAA11	AU13	SA_MA[11]	SA_DS[7]	AK2	MDA7			
	MAAA12	AW11	SA_MA[12]						
	MAAA13	AU24	SA_MA[13]	SA_DS[11]	AP2	DQSA1			
	MAAA14	AT11	SA_MA[14]	SA_DS[11]	AP3	-DQSA1			
	MAAA15	AR10	SA_MA[15]	SA_DM[1]	AN1	DMA1			
[7]	-SWEA	AT22C	SA_WE#	SA_DS[8]	AN3	MDA8			
[7]	-SCASA	AU22C	SA_CAS#	SA_DS[9]	AN2	MDA9			
[7]	-SRASA	AT20C	SA_RAS#	SA_DS[10]	AR3	MDA10			
[7]	SBAA0	AV20	SA_BS[0]	SA_DS[11]	AR2	MDA11			
[7]	SBA01	SBA01	AU19	SA_DS[12]	AM3	MDA12			
[7]	SBA02	SBA02	AU12	SA_DS[13]	AM2	MDA13			
				SA_DS[14]	AR4	MDA15			
				SA_DS[15]					
[7]	-CSA0	AV21C	SA_CS#	SA_DS[2]	AJ4	DQSA2			
[7]	-CSA1	AW24C	SA_CS#	SA_DS[2]	AJ3	-DQSA2			
		AU23C	SA_CS#	SA_DS[3]	AJ1	DMA2			
[7]	CKEA0	AU10	SA_CKE[0]	SA_DS[16]	AT4	MDA16			
[7]	CKEA1	AW10	SA_CKE[1]	SA_DS[17]	AJ2	MDA17			
		AU10	SA_CKE[2]	SA_DS[18]	AW3	MDA18			
		AY10	SA_CKE[3]	SA_DS[19]	AW4	MDA19			
				SA_DS[20]	AT3	MDA20			
		AV23	SA_ODT[0]	SA_DS[21]	AT1	MDA21			
		AV23	SA_ODT[1]	SA_DS[22]	AV2	MDA22			
		AY24	SA_ODT[2]	SA_DS[23]	AV4	MDA23			
				SA_DS[3]	AY6	DQSA3			
				SA_DS[3]	AW6	-DQSA3			
				SA_DM[3]	AV6	DMA3			
[7]	DCLKA0	AR22	SA_CK[0]	SA_DS[4]	AW5	MDA24			
[7]	-DCLKA0	AR21C	SA_CK#	SA_DS[5]	AY5	MDA25			
[7]	DCLKA1	AP18	SA_CK[1]	SA_DS[24]	AU5	MDA26			
[7]	-DCLKA1	AN18C	SA_CK#	SA_DS[25]	AJ8	MDA27			
		AN21	SA_CK[2]	SA_DS[26]	AY8	MDA27			
		AP21C	SA_CK#	SA_DS[27]	AY8	MDA28			
		AP19	SA_CK[3]	SA_DS[28]	AY5	MDA29			
		AN19C	SA_CK#	SA_DS[29]	AV7	MDA30			
				SA_DS[30]	AW7	MDA31			
				SA_DS[31]					
[7,8]	-DDR3_RST	AV8	SM_DRAMRST#		AR28	DQSA4			
		AY22C	SA_CS#	SA_DS[4]	AT29	-DQSA4			
		AY23C	SA_CS#	SA_DS[4]	AN29	DMA4			
		AY23C	SA_CS#						
		AY23C	SA_CS#						
		AY23C	SA_CS#						
		AY23C	SA_CS#						
		AY23C	SA_CS#						
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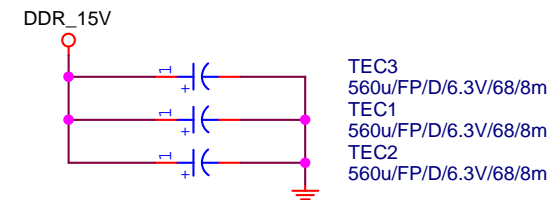
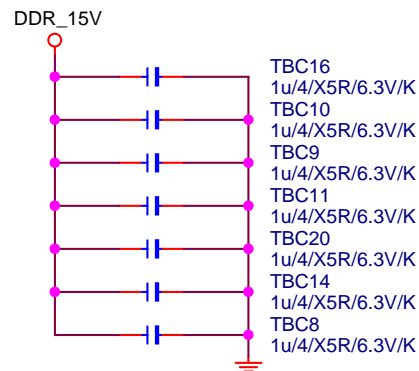
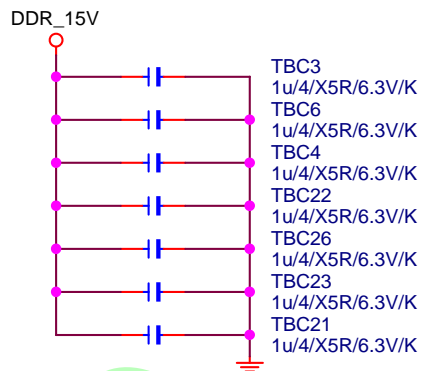
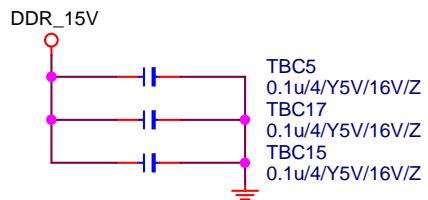




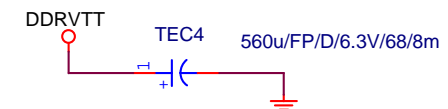
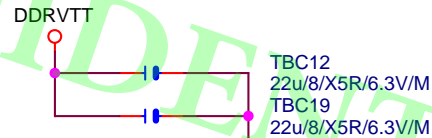
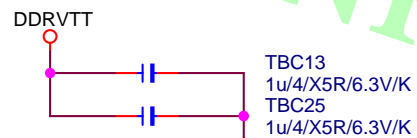
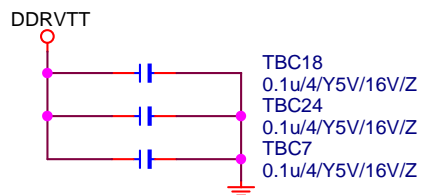


DDR TERMINATION CHANNEL A/B

DDR15V Decouple



DDRVTT Decouple



REF VCC層GND, GND層GND要塞孔



REF GND層GND, VCC層GND要塞孔

Gigabyte Technology		
Title		
DDRIII POWER CAP		
Size A	Document Number GA-H55M-S2	Rev 1.3
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DMI:12/5/5/5/12
Impedance=80 +- 17.5%

PCIE X1 :15/5/5/5/15
Impedance=80 +- 17.5%

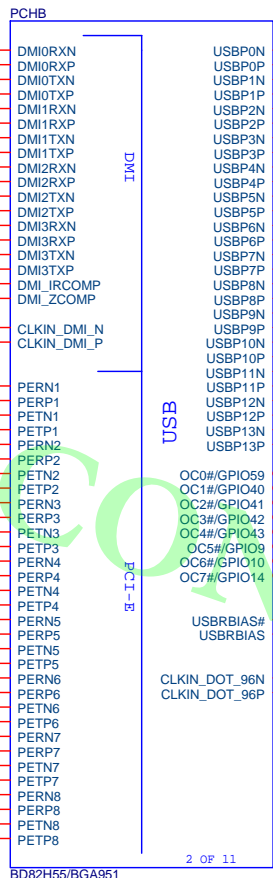
PCIE X1

LAN

PCH_HS

COST DOWN H/S(GIGABYTE LOGO)

PCH_HS[12SP2-030030-D1R_12SP2-030030-D2R_12SP2-030030-D3R]



USB:15/4.5/7.5/4.5/15
Impedance=90+- 15%

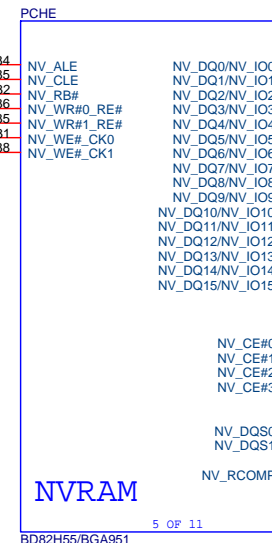
H55-->PORT6,7:N/A

OC[3:0]# for
Device 29
(ports 0-7)
OC[7:4]# for
Device 26
(ports 8-13)

USB OC# Configure	
OC0#	USB0,1(F_USB1)
OC1#	USB2,3(F_USB2)
OC2#	USB4,5(USB_LAN)
OC3#	USB6,7(X)H55-->N/A
OC4#	USB8,9(R_USB)
OC5#	USB10~11(R_USB)
OC6#	USB12~13(KB_USB)
OC7#	GPIO14

NV_ALE	
Hi	Enable Danbury
Lo	Disable Danbury

Intel anti theft techonlogy



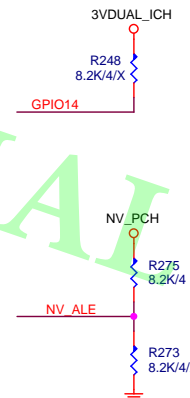
Impedance=50+- 15%

ONFI: NV_DQ 4/5

NV_DQS 4/10

NV_CTRL 4/10

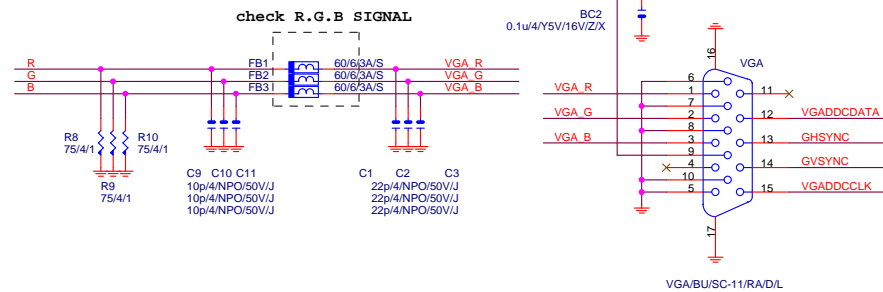
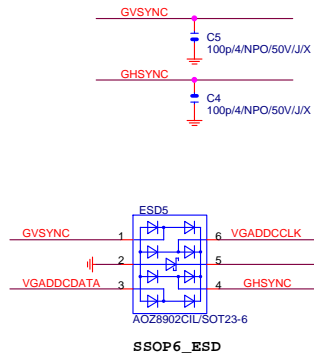
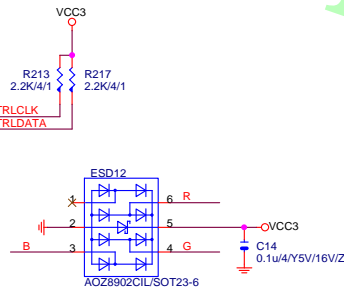
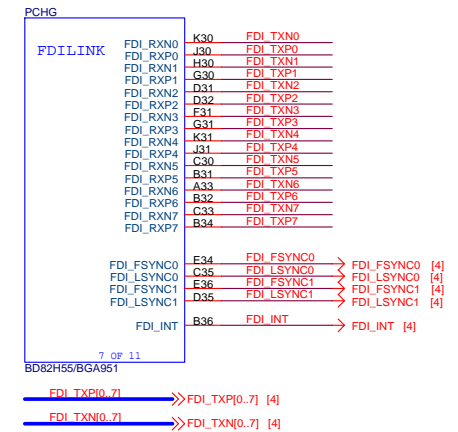
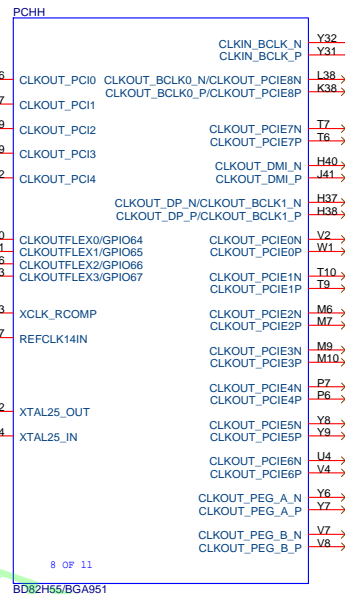
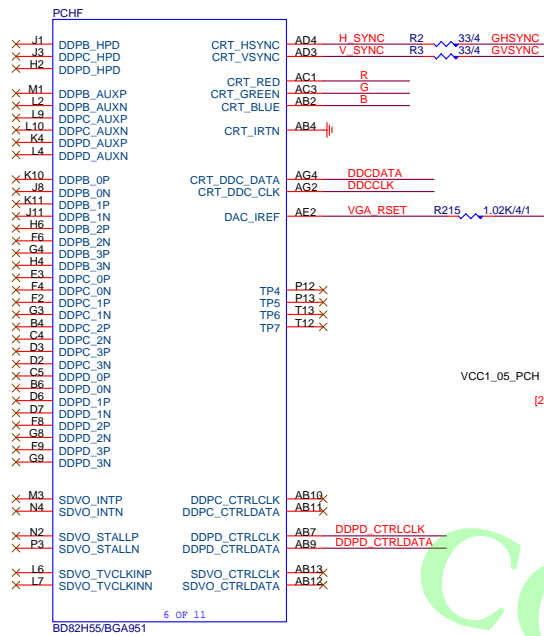
NV_CK 4/15



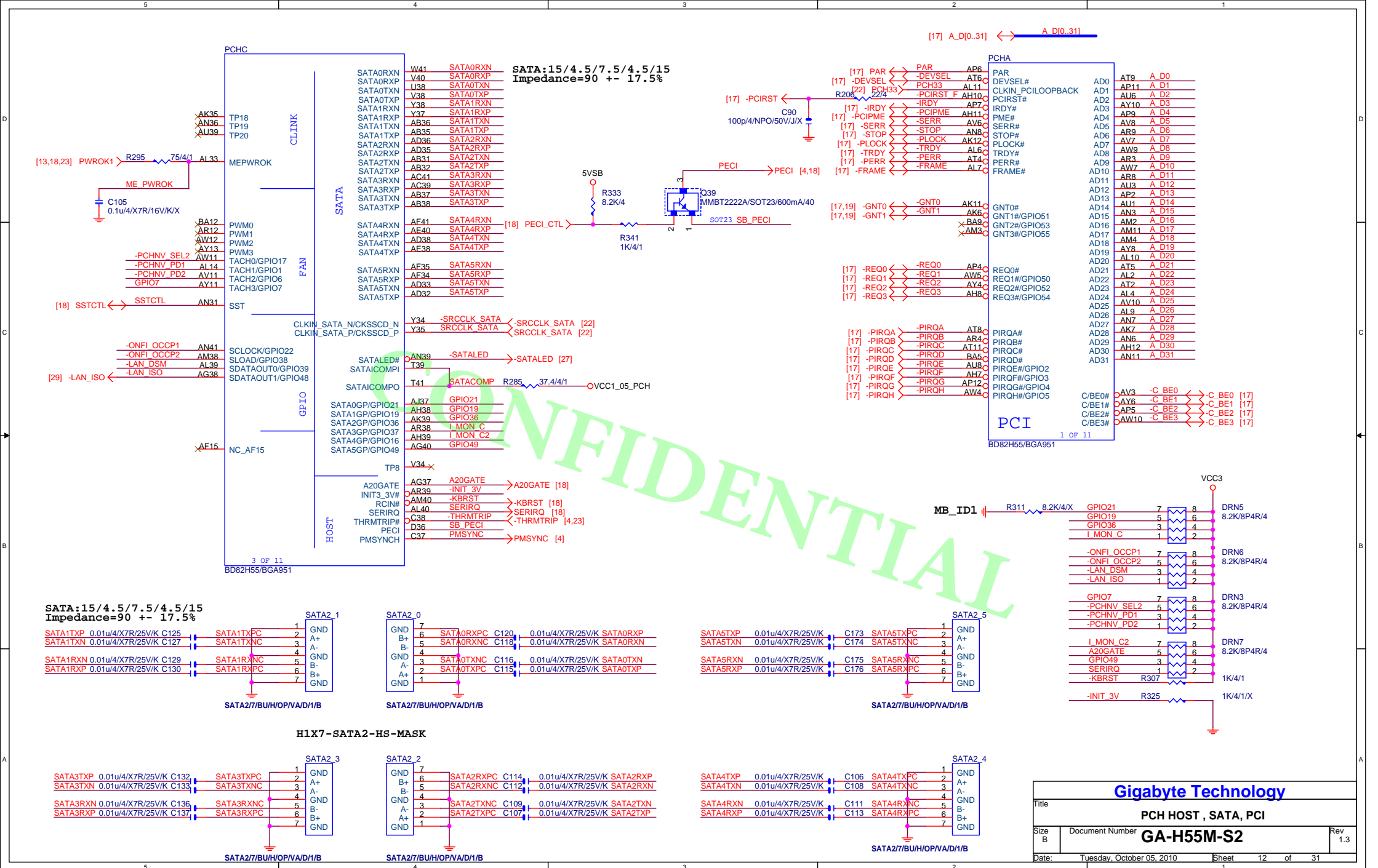
DMI Terminator voltage
HI : AC COUP : TX/RX TO VCC
LO : DC COUP : HALF SWING

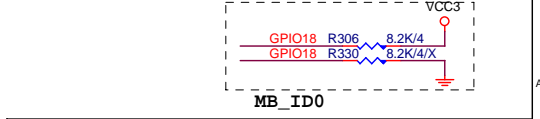
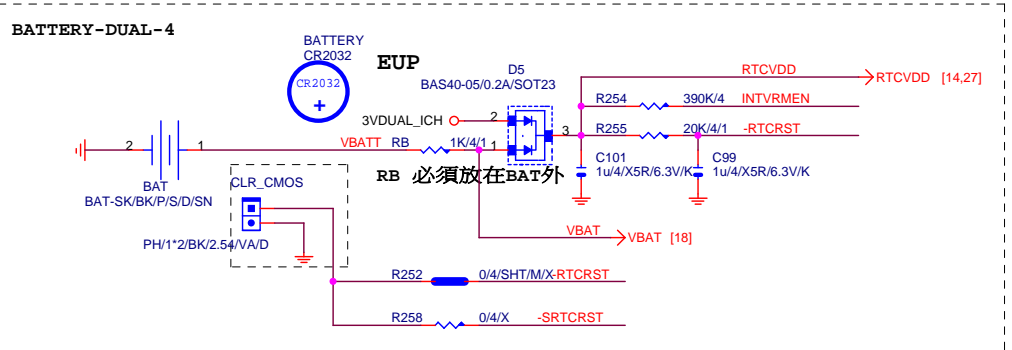
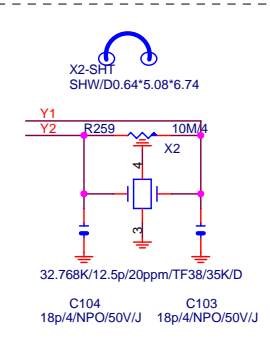
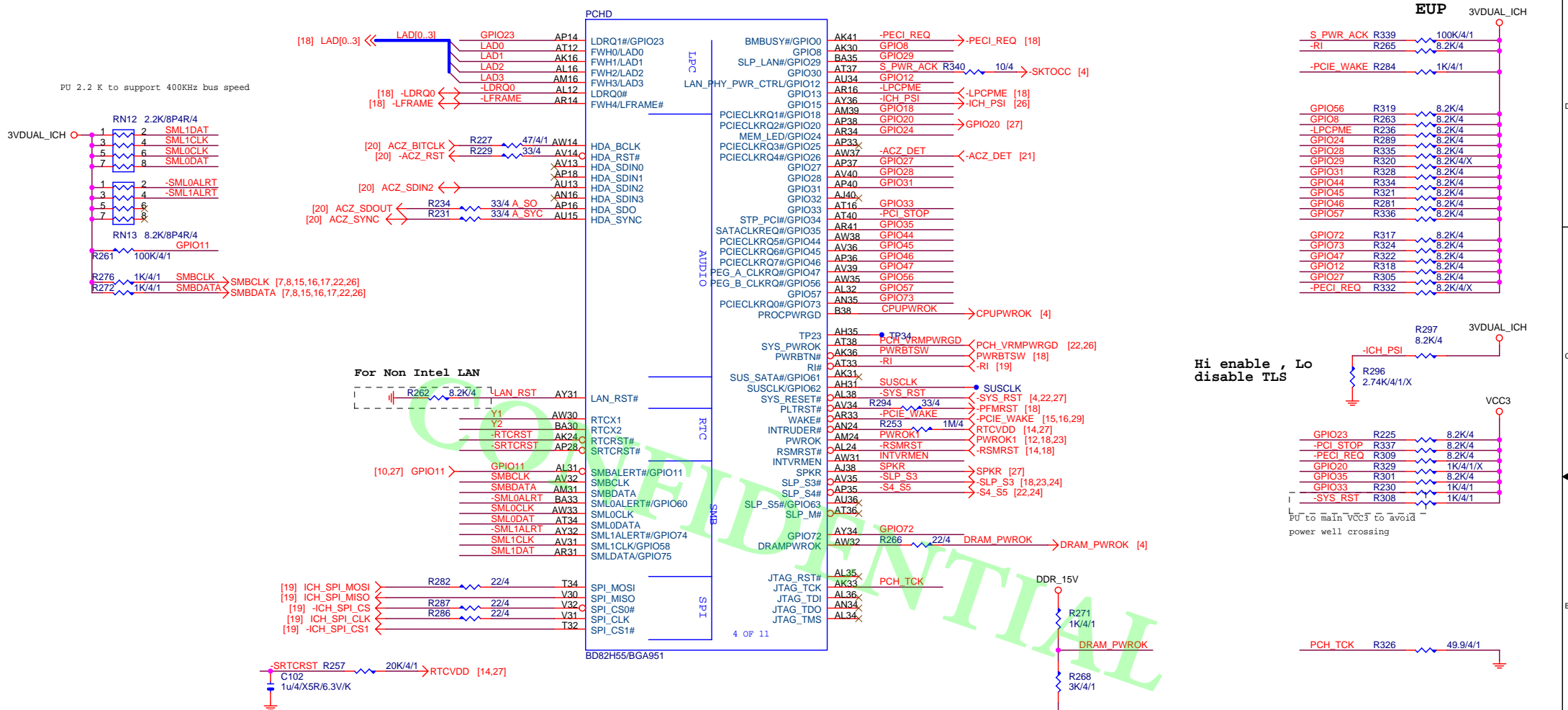
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Title		
PCH FDI,DMI,USB,PCIE,NVRAM		
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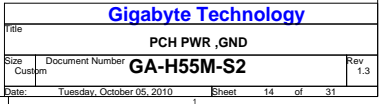


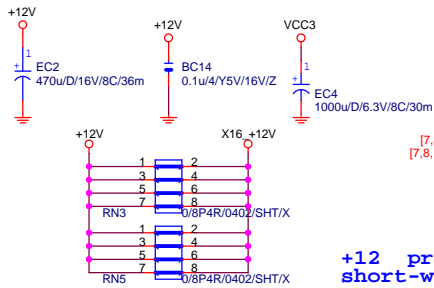
Gigabyte Technology			
Title			
PCH DISPLAY, CLK BUFFER			
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Gigabyte Technology			
PCH GPIO , CTRL , AUDIO			
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+12 protect
short-wire test

EXP_RXP[0..15] >> EXP_RXP[0..15] [4]
EXP_RXN[0..15] >> EXP_RXN[0..15] [4]
EXP_TXP[0..15] >> EXP_TXP[0..15] [4]
EXP_TXN[0..15] >> EXP_TXN[0..15] [4]

PCIE16:15/4/8/4/15

EXP_TXP0	C42	0.1u/4/X7R/16V/K	EXP_TXP0C
EXP_TXN0	C43	0.1u/4/X7R/16V/K	EXP_TXN0C
EXP_TXP1	C45	0.1u/4/X7R/16V/K	EXP_TXP1C
EXP_TXN1	C48	0.1u/4/X7R/16V/K	EXP_TXN1C
EXP_TXP2	C53	0.1u/4/X7R/16V/K	EXP_TXP2C
EXP_TXN2	C54	0.1u/4/X7R/16V/K	EXP_TXN2C
EXP_TXP3	C56	0.1u/4/X7R/16V/K	EXP_TXP3C
EXP_TXN3	C58	0.1u/4/X7R/16V/K	EXP_TXN3C
EXP_TXP4	C59	0.1u/4/X7R/16V/K	EXP_TXP4C
EXP_TXN4	C60	0.1u/4/X7R/16V/K	EXP_TXN4C
EXP_TXP5	C62	0.1u/4/X7R/16V/K	EXP_TXP5C
EXP_TXN5	C63	0.1u/4/X7R/16V/K	EXP_TXN5C
EXP_TXP6	C65	0.1u/4/X7R/16V/K	EXP_TXP6C
EXP_TXN6	C66	0.1u/4/X7R/16V/K	EXP_TXN6C
EXP_TXP7	C67	0.1u/4/X7R/16V/K	EXP_TXP7C
EXP_TXN7	C68	0.1u/4/X7R/16V/K	EXP_TXN7C
EXP_TXP8	C69	0.1u/4/X7R/16V/K	EXP_TXP8C
EXP_TXN8	C70	0.1u/4/X7R/16V/K	EXP_TXN8C
EXP_TXP9	C71	0.1u/4/X7R/16V/K	EXP_TXP9C
EXP_TXN9	C72	0.1u/4/X7R/16V/K	EXP_TXN9C
EXP_TXP10	C74	0.1u/4/X7R/16V/K	EXP_TXP10C
EXP_TXN10	C76	0.1u/4/X7R/16V/K	EXP_TXN10C
EXP_TXP11	C77	0.1u/4/X7R/16V/K	EXP_TXP11C
EXP_TXN11	C78	0.1u/4/X7R/16V/K	EXP_TXN11C
EXP_TXP12	C80	0.1u/4/X7R/16V/K	EXP_TXP12C
EXP_TXN12	C82	0.1u/4/X7R/16V/K	EXP_TXN12C
EXP_TXP13	C84	0.1u/4/X7R/16V/K	EXP_TXP13C
EXP_TXN13	C86	0.1u/4/X7R/16V/K	EXP_TXN13C
EXP_TXP14	C87	0.1u/4/X7R/16V/K	EXP_TXP14C
EXP_TXN14	C89	0.1u/4/X7R/16V/K	EXP_TXN14C
EXP_TXP15	C91	0.1u/4/X7R/16V/K	EXP_TXP15C
EXP_TXN15	C94	0.1u/4/X7R/16V/K	EXP_TXN15C

PCI-E REV:1.1--> 2.5GHZ

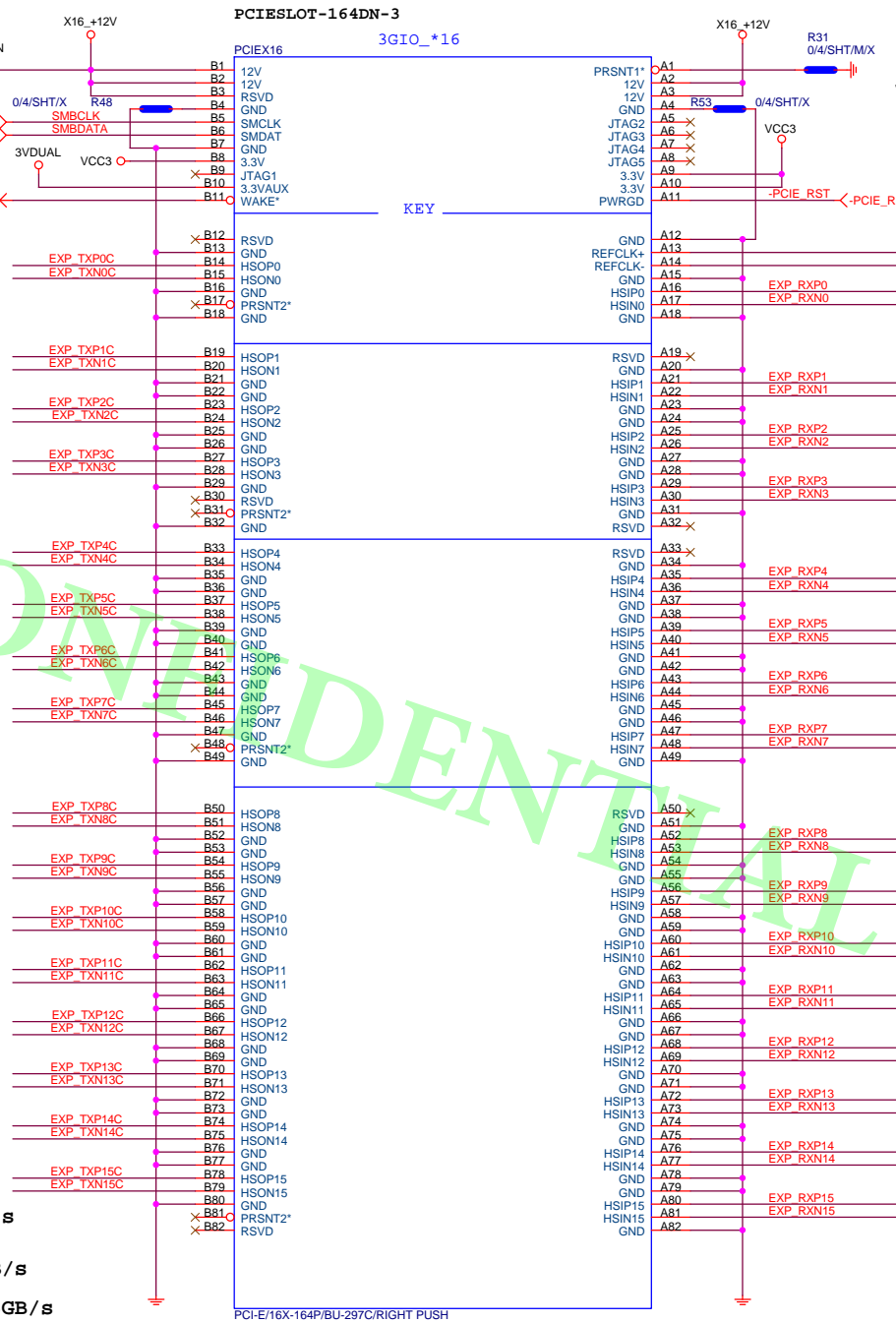
PCE-E X1(單向) BANDWITH=2.5GHz*(8b/10b)=2Gb/s=250MB/s

PCE-E X1(雙向) BANDWITH=2.5GHz*(8b/10b)X2=4Gb/s=500MB/s

PCE-E X16(單向) BANDWITH=2.5GHz*(8b/10b)X16=32Gb/s=4GB/s

PCE-E X16(雙向) BANDWITH=2.5GHz*(8b/10b)X16X2=64Gb/s=8GB/s

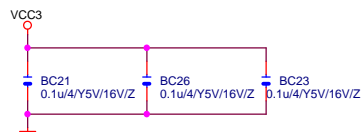
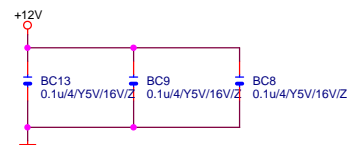
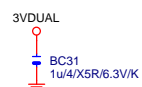
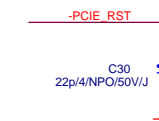
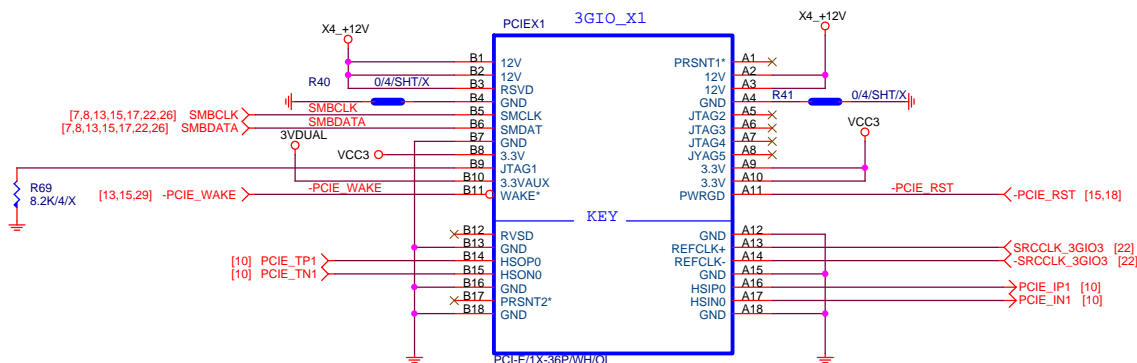
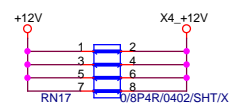
PCI-E REV:2.0--> 5GHZ



PCI-E/16X-164P/BU-297C/RIGHT PUSH

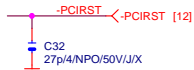
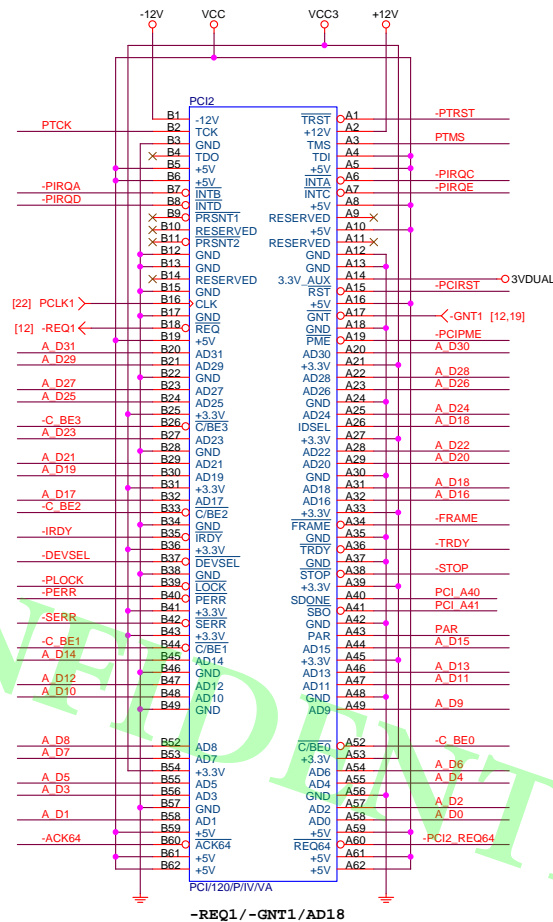
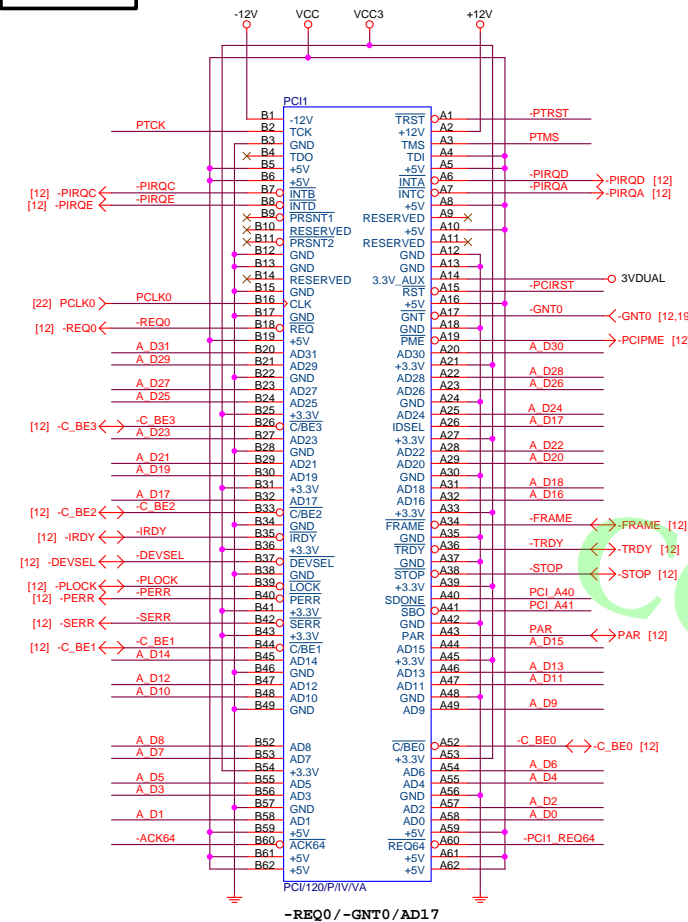
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Title			PCI EXPRESS * 16	
Size			Document Number	
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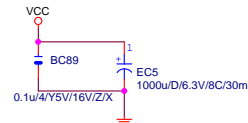
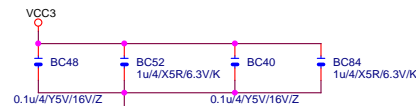
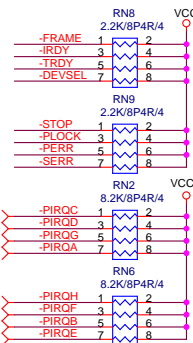
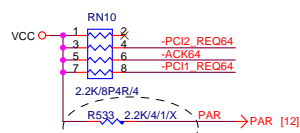
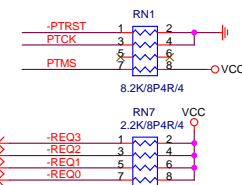


CONFIDENTIAL

PCI1,2 SLOT



Place close to PCI1



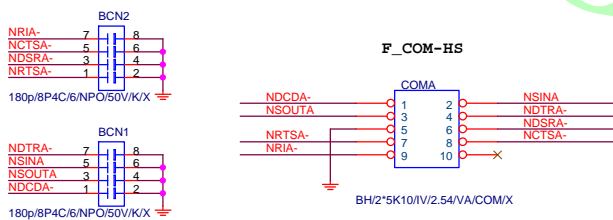
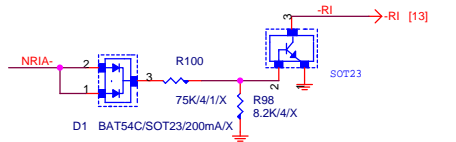
Gigabyte Technology

PCI SLOT 1, 2

GA-H55M-S2

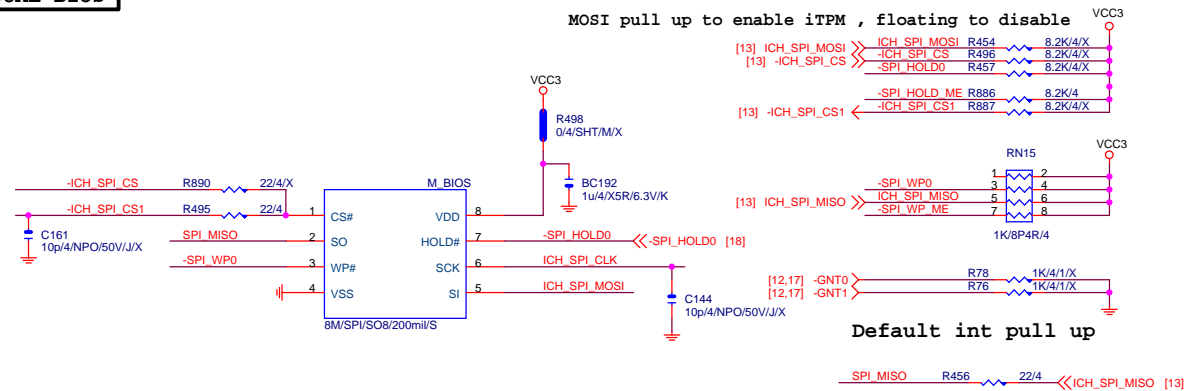
Rev
1.3

RING IN



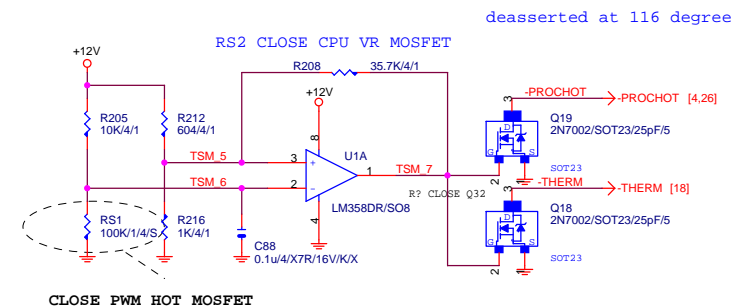
PLACE NEAR COM CONNECTOR

-PROHOT



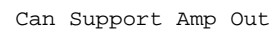
BOOT DEVICE	GNT1	GNT0
LPC	0	0
PCI	0	1
SPI	1	1

IC8SO-SOCKET

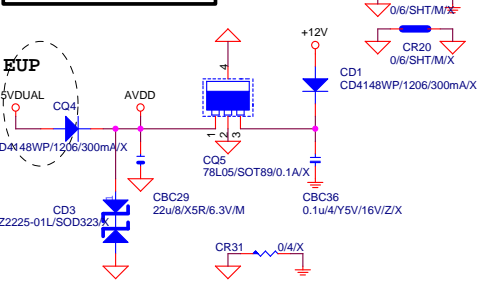


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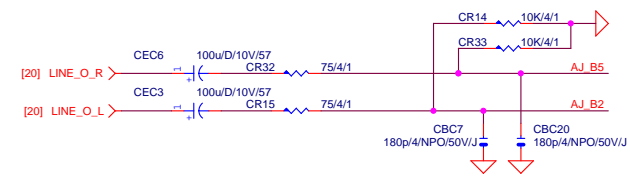
20K/4/0.1% @ALC889A
20K/4/1% @ALC889A+/ALC888Vx



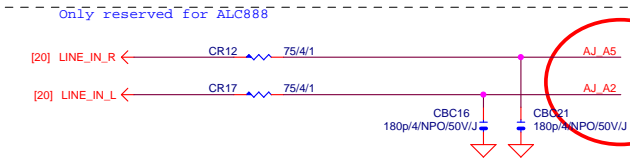
CODEC POWER/EMI PAD



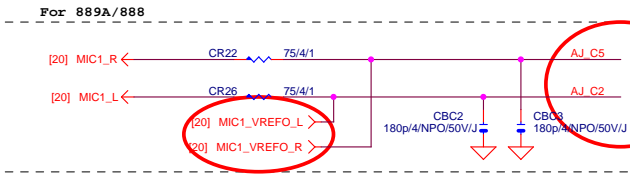
LINE-OUT



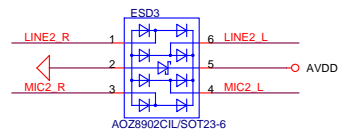
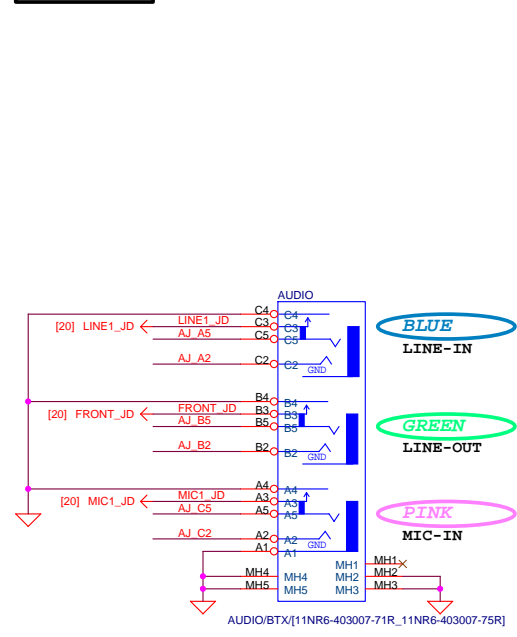
LINE-IN



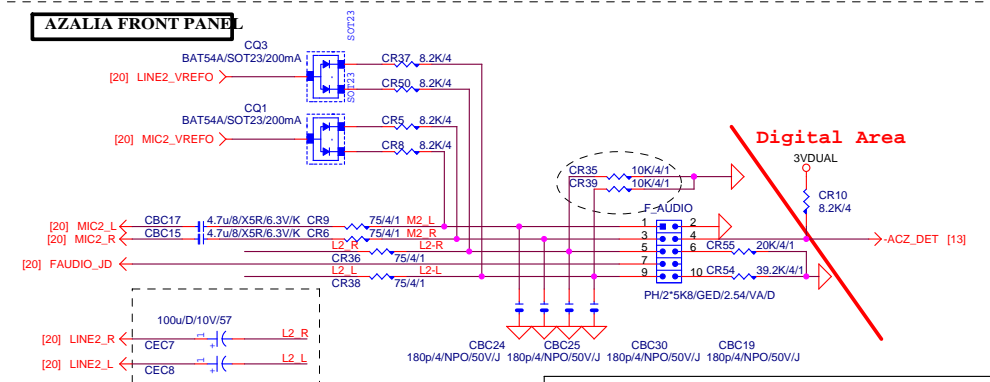
MIC-IN



AZALIA JACK



AZALIA FRONT PANEL

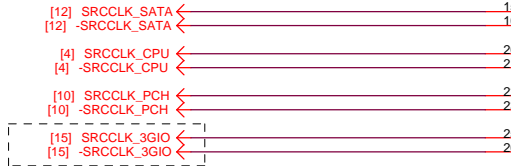


Gigabyte Technology			
Title			
AUDIO JACK			
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50歐姆:[18/4/10/4/18]



50歐姆:[18/4/10/4/18]



[16] SRCCLK_3GIO3

[16] -SRCCLK_3GIO3

[29] SRCCLK_LAN

[29] -SRCCLK_LAN

[17] PCLK1

[17] PCLK0

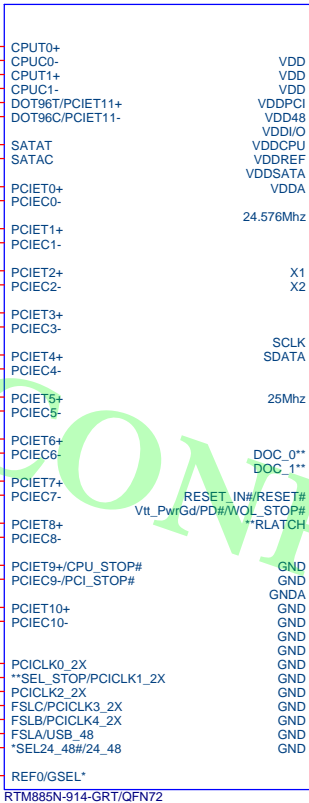
[18] LPC33

[12] PCH33

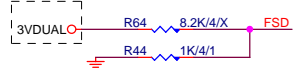
[18] LPCCLK48

[11] PCHCLK14

CLK



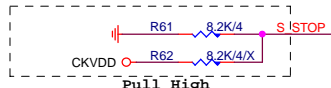
50歐姆:[4/10]



To enhance PD strength

CKVDD $\xrightarrow{R58}$ GSEL GSEL=1,96Mhz from 12/13
 GSEL=0,100Mhz from 12/13

$\xrightarrow{R77}$ SEL_48 SEL_48=1, 24Mhz from pin10
 SEL_48=0, 48Mhz from pin10



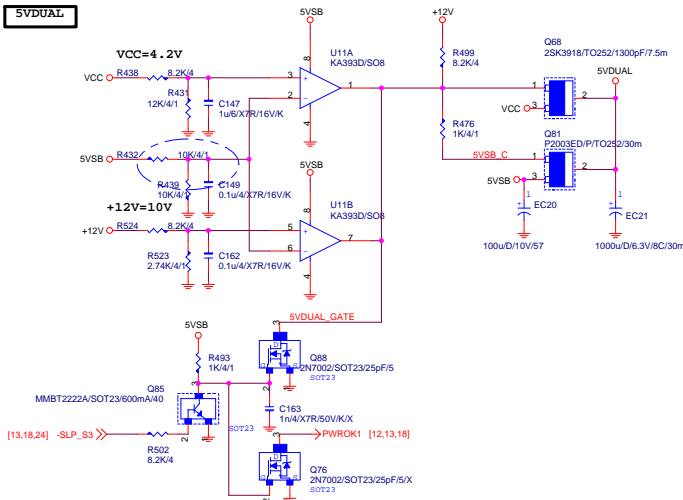
SEL_STOP: latched input to select pin functionality
 1 = Selects pin 44/45 to be PCI_STOP#/CPU_STOP#
 0 = Selects pin 44/45 to be PCIE outputs ;
 3.3V PCICLK output

FSC	FSB	FSA	CPU
0	0	0	266MHz
0	0	1	133MHz
0	1	0	200MHz
0	1	1	166MHz
1	0	0	333MHz
1	1	0	400MHz

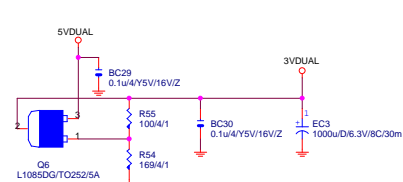
Gigabyte Technology

Title			CK505 CLK GEN
Size	Document Number	GA-H55M-S2	
Custom		Rev	1.3
Date:	Tuesday, October 05, 2010	Sheet	22 of 31

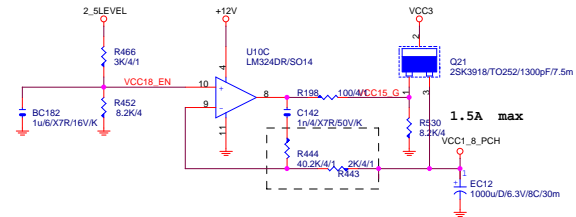
5VDUAL



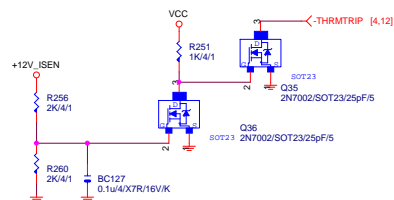
3VDUAL



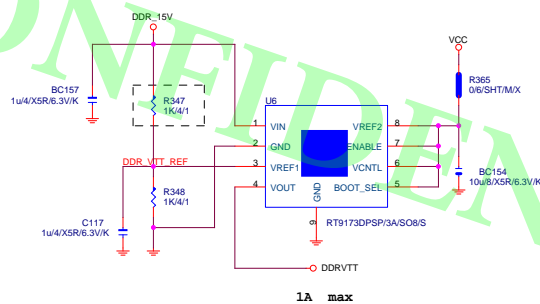
VCC1_8_PCH



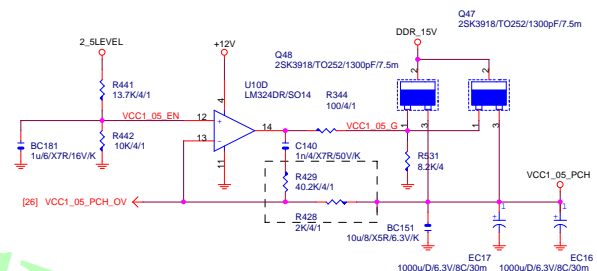
+12V SHORT PROTECT



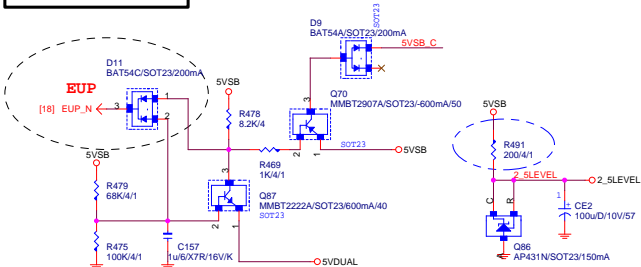
DDR_VTT



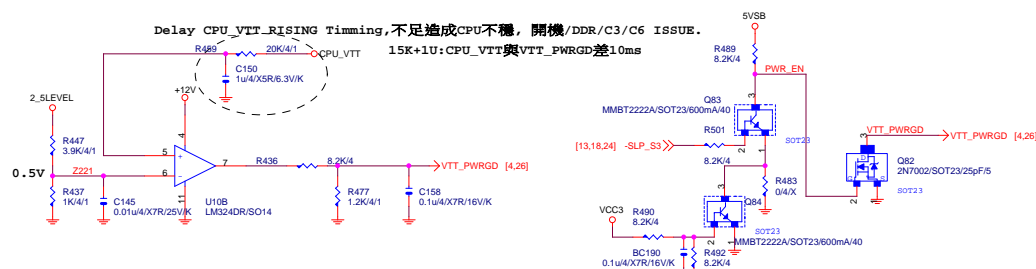
VCC1_05_PCH



5VDUAL SHORT PROTECT



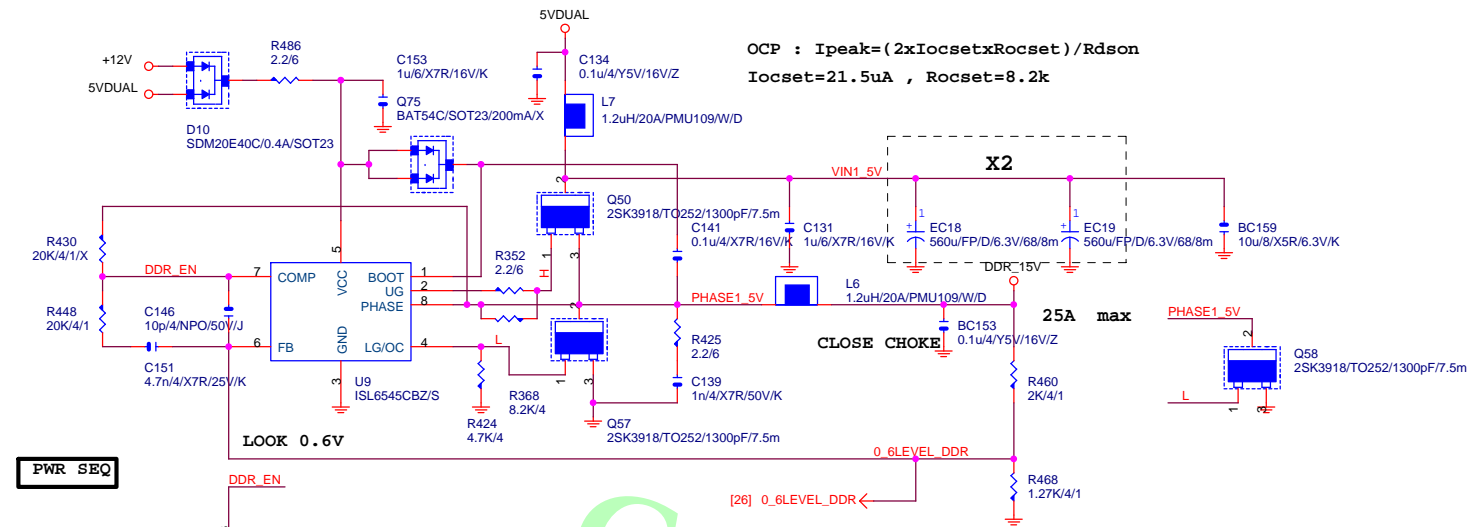
Delay CPU_VTT_RISING Timing,不足造成CPU不穩,開機/DDR/C3/C6 ISSUE.
15K+1U:CPU_VTT與VTT_PWRGD差10ms



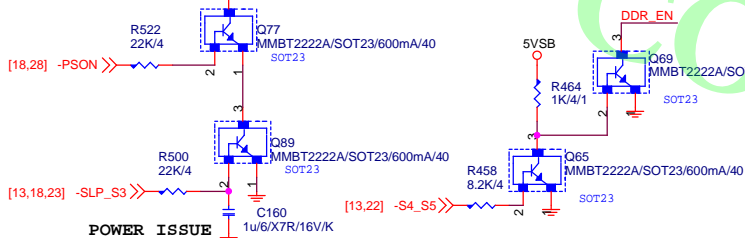
Gigabyte Technology

File		
DISCRETE POWER		
Size	Document Number	Rev
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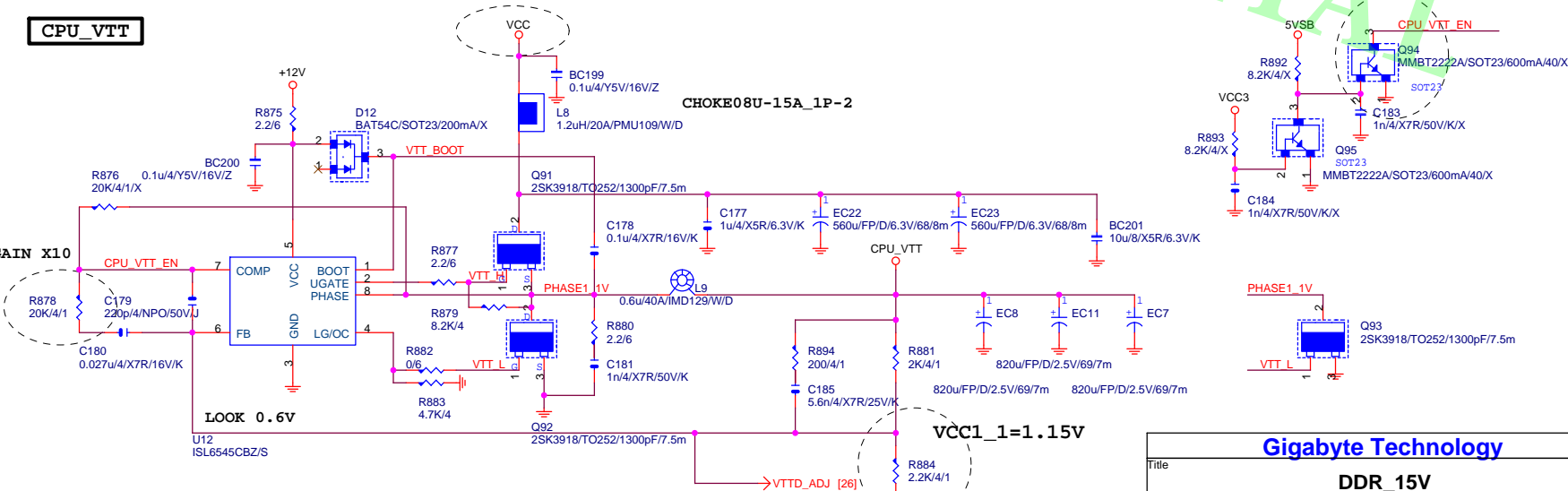
DDR1_5V



PWR SEQ

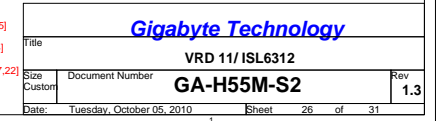


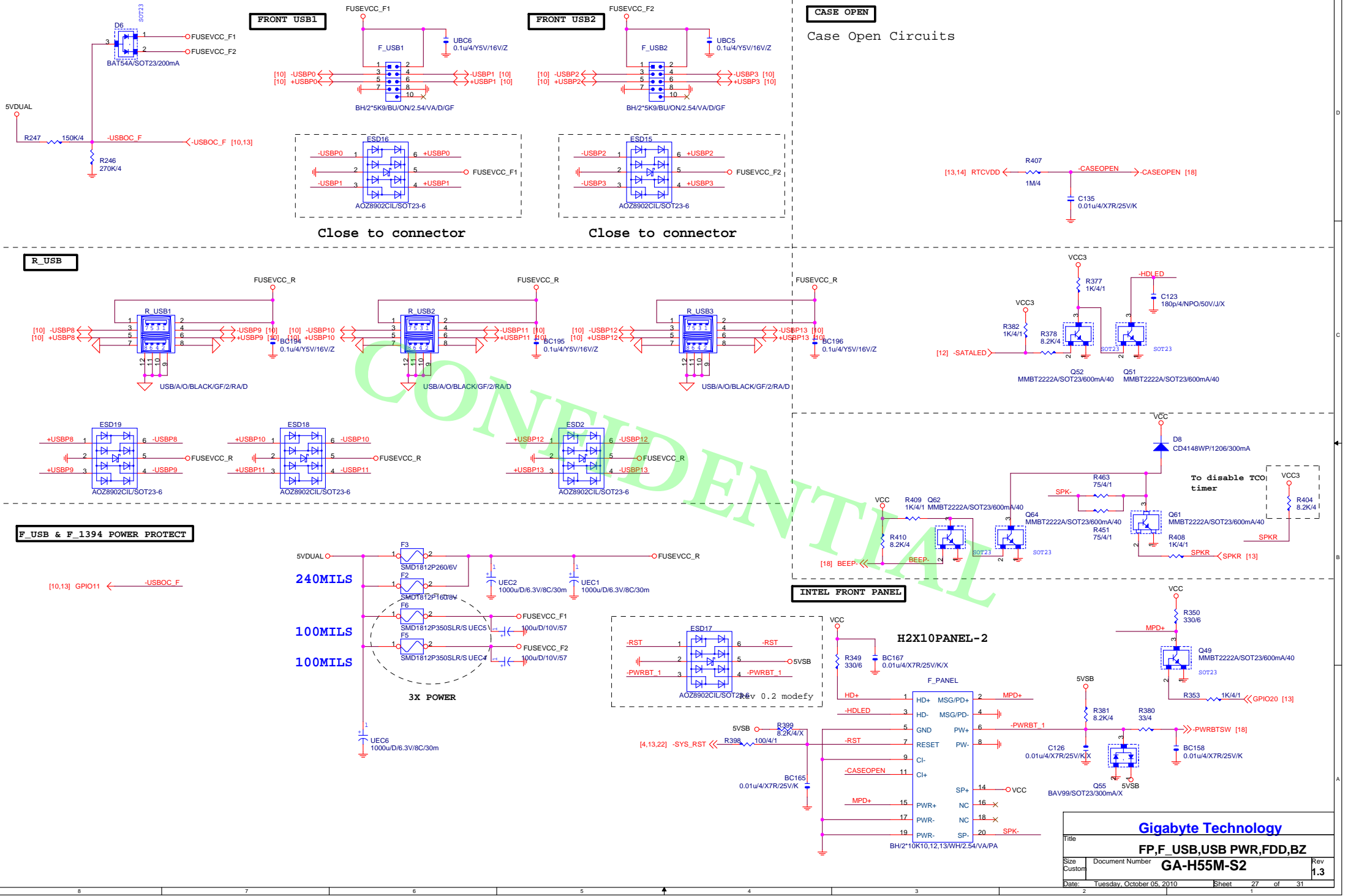
CPU_VTT



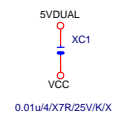
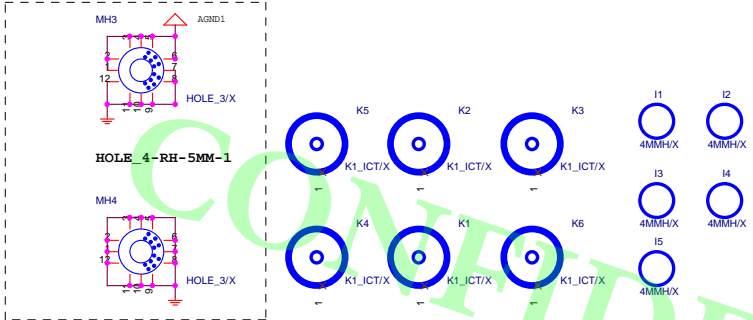
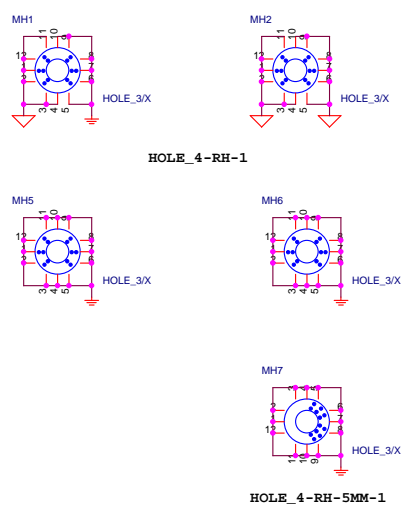
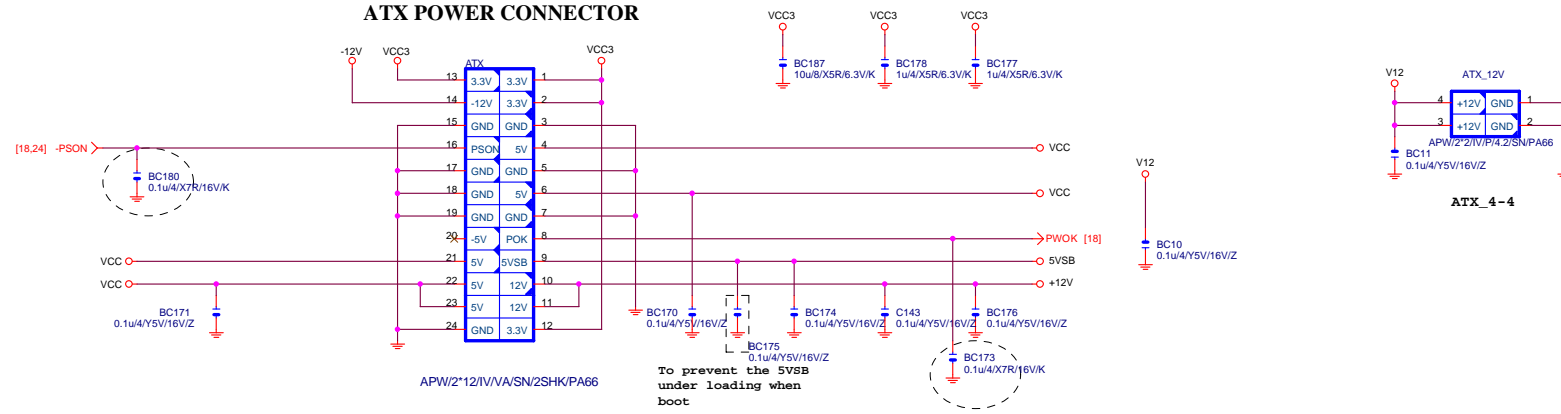
Gigabyte Technology

Title			DDR_15V
Size	Document Number	GA-H55M-S2	
Custpm		Rev	1.3
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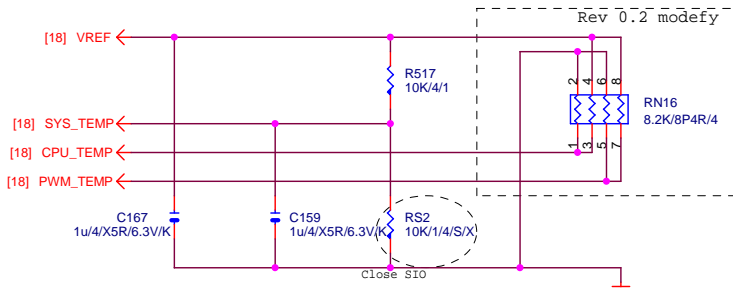




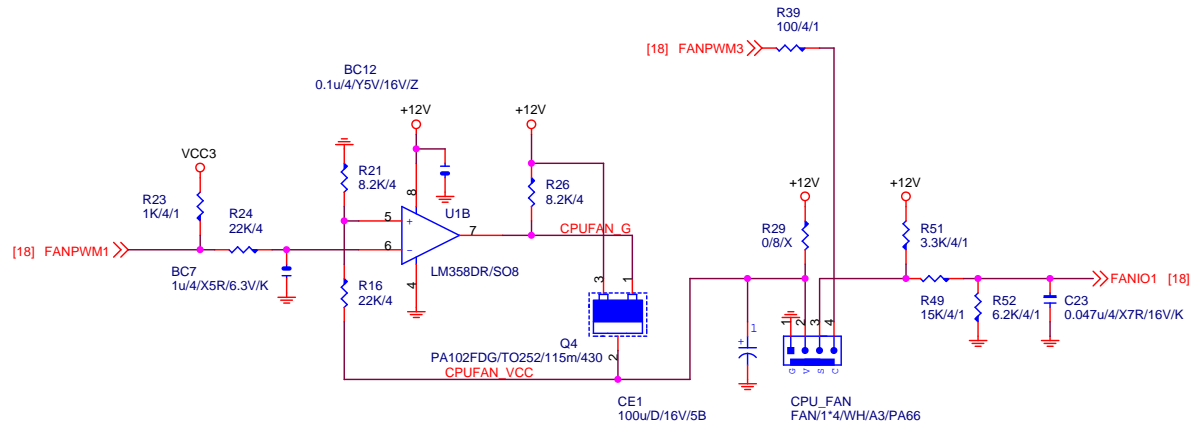
ATX POWER CONNECTOR



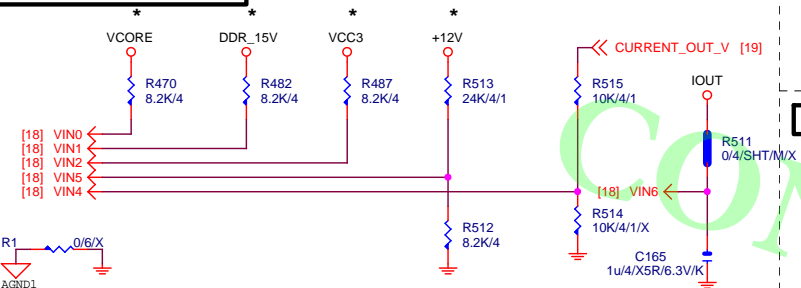
TEMP H/W MONITOR



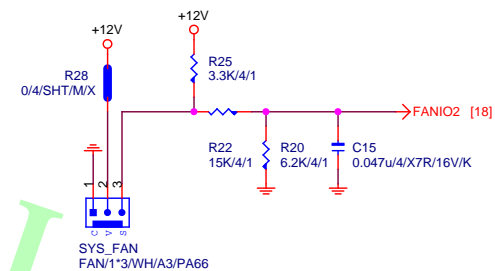
CPU SMART FAN



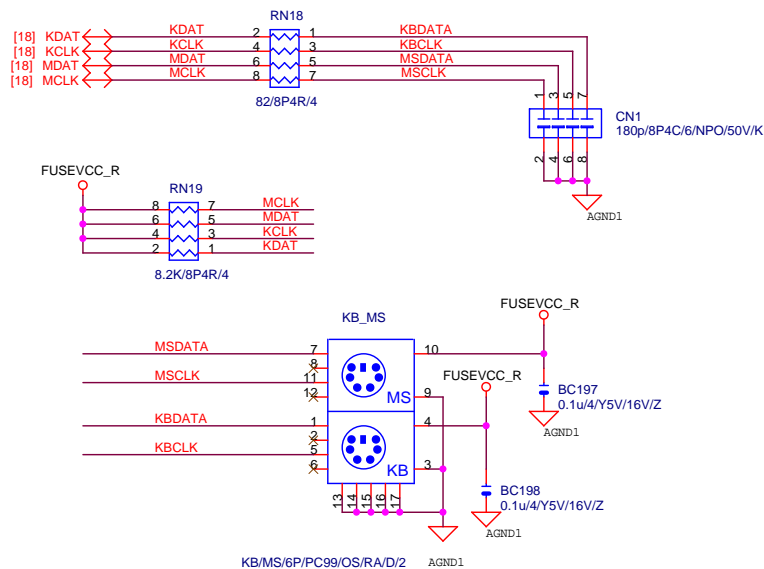
VOLTAGE-- H/W MONITOR



SYS SMART FAN Linear SYS_FAN



KB/MS



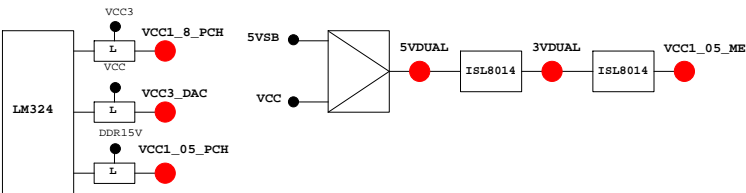
Gigabyte Technology

Title		HWM,KB/MS, FAN CTRL	
Size	Document Number	GA-H55M-S2	
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PIN NAME	PWR	AFTER HOLDING	Default	USAGE	NOTE
GP0	MAIN	H-Z	GPI	-PECTI_REQ	N/A
GP1/TACH1	MAIN		GPI	ICH_FAN_TACH1	N/A
GP2/PIRQE#	MAIN		GPI	-PIRQE	P/U 8.2K VCC3
GP3/PIRQF#	MAIN		GPI	-PIRQF	P/U 8.2K VCC3
GP4/PIRQG#	MAIN		GPI	-PIRQG	P/U 8.2K VCC3
GP5/PIRQH#	MAIN		GPI	-PIRQH	P/U 8.2K VCC3
GP6/TACH2	MAIN		GPI	ICH_FAN_TACH2	N/A
GP7/TACH3	MAIN		GPI	ICH_FAN_TACH3	N/A
GP8	STBY	H	GPO	GPIO8	P/U 8.2K 3VDUAL
GP9/OC5#	STBY		NATIVE	OC5#	N/A
GP10/OC6#	STBY		NATIVE	OC6#	N/A
GP11/SMBALERT#	STBY		NATIVE	-SMBALERT	P/U 8.2K 3VDUAL
GP12	STBY	L	GPI	LAN_PHY_PWR_CTRL	P/U 8.2K 3VDUAL
GP13	STBY	L	GPI	GPIO13	P/U 8.2K 3VDUAL
GP14/OC7#	STBY		NATIVE	OC7#	N/A
GP15	STBY	L	GPO	GPIO15	N/A
GP16	MAIN		GPI	-SKTOCC	P/U 8.2K VCC3
GP17/TACH0	MAIN		GPI	ICH_FAN_TACH0	N/A
GP18	MAIN		NATIVE	MB_ID0	P/D 8.2K GND
GP19	MAIN		GPI	-LAN1_ISO	P/U 8.2K VCC3
GP20	MAIN		NATIVE	LED_CTL	P/U 1K VCC3
GP21	MAIN		GPI	VCC18_PCH_OV2	P/U 8.2K VCC3
GP22	MAIN	H-Z	GPI	VCORE_OV3	P/U 8.2K VCC3
GP23	MAIN		NATIVE	-LDRQ1	P/U 8.2K VCC3
GP24	STBY	L	GPO	TL5	P/U 8.2K 3VDUAL
GP25	STBY		NATIVE	-CPU_STOP	P/U 8.2K 3VDUAL
GP26	STBY		NATIVE	-ACZ_DET	P/U 8.2K 3VDUAL
GP27	STBY	H	GPO	GPIO27	P/U 8.2K 3VDUAL
GP28	STBY	H	GPO	GPIO28	P/U 8.2K 3VDUAL
GP29	STBY	L	GPI	GPIO29	N/A
GP30	STBY	H-Z	GPI	S_PWR_ACK	P/U 100K 3VDUAL
GP31	STBY	H-Z	GPI	N/A(Reverse)	P/U 8.2K VCC3
GP32	MAIN	H	GPO	MB_ID1	P/D 8.2K GND
GP33	MAIN	H	GPO	LOAD-LINE	P/U 1K VCC3
GP34	MAIN	H-Z	GPI	-PCI_STOP	P/U 8.2K VCC3
GP35	MAIN	L	GPO	GPIO35	P/U 8.2K VCC3
GP36	MAIN		GPI	-LAN1_DSM	P/U 8.2K VCC3
GP37	MAIN		GPI	N/A	P/U 8.2K VCC3
GP38	MAIN	H-Z	GPI	VCORE_OV2	P/U 8.2K VCC3
GP39	MAIN	H-Z	GPI	-LAN_DSM	P/U 8.2K VCC3
GP40	STBY		NATIVE	OC1#	N/A
GP41	STBY		NATIVE	OC2#	N/A
GP42	STBY		NATIVE	OC3#	N/A
GP43	STBY		NATIVE	OC4#	N/A
GP44	STBY	L	NATIVE	N/A	P/U 8.2K 3VDUAL
GP45	STBY		NATIVE	-LPCPME	P/U 8.2K 3VDUAL
GP46	STBY	L	NATIVE	PWR_LED	P/U 8.2K 3VDUAL
GP47	STBY		NATIVE	PSI_LED	P/U 8.2K 3VDUAL
GP48	MAIN	H-Z	IN	EN_PWM	P/U 8.2K VCC3
GP49	MAIN	H-Z	IN	VCC18_OV1	P/U 8.2K VCC3
GP50	MAIN		NATIVE	-REQ1	P/U 2.2K VCC
GP51	MAIN	H	NATIVE	-GNT1	N/A
GP52	MAIN		NATIVE	-REQ2	P/U 2.2K VCC
GP53	MAIN	H	NATIVE	-GNT2	N/A
GP54	MAIN		NATIVE	-REQ3	P/U 2.2K VCC
GP55	MAIN	H	NATIVE	-GNT3	N/A
GP56	STBY		NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL
GP57	STBY	H-Z	IN	VCORE_OV1	P/U 8.2K 3VDUAL
GP58	STBY	H-Z	NATIVE	F_USB_OC	P/U 8.2K 3VDUAL
GP59	STBY		NATIVE	USB_OC0#	N/A
GP60	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL
GP61	STBY	L	NATIVE	-SUSSTAT	N/A
GP62	STBY	L	NATIVE	SUSCLK	N/A
GP63	STBY	L	NATIVE	GPIO63	N/A
GP64	MAIN	L	NATIVE	CLKOUTFLEX0	N/A
GP65	MAIN	L	NATIVE	CLKOUTFLEX1	N/A
GP66	MAIN	L	NATIVE	CLKOUTFLEX2	N/A
GP67	MAIN	L	NATIVE	CLKOUTFLEX3	N/A
GP72	STBY	H-Z	NATIVE	VCORE_OV4	P/U 8.2K 3VDUAL
GP73	STBY		NATIVE	1_05V_OV1	P/U 8.2K 3VDUAL
GP74	STBY	H-Z	NATIVE	1_05V_OV2	P/U 8.2K 3VDUAL
GP75	STBY	H-Z	NATIVE	N/A(Reverse)	P/U 8.2K 3VDUAL

PIN NAME	USAGE	NOTE
SVC/PECI_RQT/GP14	-PECI_REQ	
PWROK1/GP13	PWROK1/ITE_PWROK	
KRST#/GP62	-KBRST	
SO/GP50	-ICH_SPI_CS	
IRTX/GP47/CE2_N/JP7	CEB_N	
GP46/IRRX	-LAN2_DSM	
PSION#/GP42	-PSON	
PWROK2#/GP41	PECI_CTL	
PCIRST3#/GP10/VDIMM_STR_EN	-PCIE_RST	
RSMRST#CIRRX1/GP55	-RSMRST	
FME#/GP54	-LPCPME	
PD5/GP75/BUS00	N/A	

PIN NAME	USAGE	NOTE
FAN_TAC2/GP52	FANIO2	
FAN_TAC3/GP37	FANIO3	
VIDO3/FAN_TAC4/GP25/DSR2#	FANIO4	
FAN_CTL2/GP51	FANPWM2	
FAN_CTL3/GP36	FANPWM3	
VID4/GP34	BEEP-	
VID3/GP33	TURBO1	
VID2/GP32	TURBO0	
VCORE_GOOD/VID6/GP63	CPUT_LED1_C	
VID5/GP35	CPUT_LED2_C	
VID1/GP31	CPUT_LED3_C	
VID0/GP30	-LAN1_DSM	NBT_LED1_C
SLCT/GP80	CPU_LED1_C	
PE/GP81	CPU_LED2_C	
BUSY/GP82	CPU_LED3_C	
PD3/GP73/BUSSI1	SB_LED1_C	
PD4/GP74/BUSSI2	SB_LED2_C	
VCORE_EN/VID7/GP64	IT_GP64	SB_LED3_C
PD0/GP70	NB_LED1_C	
PD1/GP71	NB_LED2_C	
PD2/GP72/BUSSI0	NB_LED3_C	
GP22/SCK	LOW_PWR_1	
VIDO5/GP27/SIN2	LOW_PWR_2	
PCIRST2#/GP11	-PFMRST1	
PCIRST1#/GP12	-PFMRST2	
3VSB5W#/GP40	CSI_F0	BSEL166_1
SUSC#/GP53	CSI_F1	BSEL166_2
GP23/SI	BSEL166_3/CSISBSL	
VIDO0/GP20/CTS2#	CPUT_LED1_C	BSEL166_4
GP65/VDDA_EN/GB_01	MB_ID2	
PD6/GP76/BUSSO1	MB_ID3	
PD7/GP77/BUSSO2	MB_ID4	
AFD#/GP86/SMBC_R	2X PIN	FST_2X8
INIT#/GP85/SMBD_M	SEC_2x8	GTLREF_AD2
ACK#/GP83	DDR_LED1_C	
VIDO1/GP21/DCD2#	DDR_LED2_C	
STB#/GP87/SMBC_M	DDR_LED3_C	
PWRON#GP44	VCORE_OV1	
PANSWH#/GP43	PWRBTSW	
KDAT/GP61	-PWRBTSW	
KCLK/GP60	KDAT	
MDAT/GP57	KCLK	
MACL/GP56	MDAT	
GP66/VLDT_EN/GB_02	NBT_LED1_C	MCLK
SVD/PCIRSTIN#/CIRTX/GP15	PWM2_CR	
KDAT/GP61	PWM2_CR	
GP67/CPU_PG/GB_03	EN_LOADLINE	IT_GP67/-EN_PWM2
SLIN#/GP84/SMBD_R	-EN_PWM2	
PSI_L/FAN_CLT5/CIRRX2/GP16	-THERM	
VIDO4/GP26/SOUT2	DDR18V_PH2_EN	
VIDO2/FAN_TAC5/GP24/DSR2#	DDR18V_LED	
VIDO6/GP17/RI2#	1_1V_PH_EN	
VIDO7/JP6/DTR2#	JP6	
PD5/GP75/BUSSO0	SB_LED3_C	

[illegible]

散熱模組料號：

線路圖名稱	BIOS選項
Vcore	CPU Vcore
CPU_VTT	CPU Termination
CPU_VAXG	CPU Graphic Core
VCC1_8_PCH	CPU PLL
VCC1_05_PCH	PCH core
3VDUAL	3VDUAL
DDR15V	DRAM voltage
DDRVRTT	DRAM Terminatio
VREF_CA_A/REF_CA_B	DRAM Address Ref
VREF_DQ_A/REF_DQ_B	DRAM Data Ref

	3 pin FAN control	4 pin FAN control	FAN speed	Controller
CPU FAN	FANPWM1	FANPWM3	FANIO1	IT8720
	ICH_FAN_PWM2	ICH_FAN_PWM0	ICH_FAN_TACH0	PCH
SYS FAN	FANPWM2	N/A	FANIO2	IT8720
	ICH_FAN_PWM1	N/A	ICH_FAN_TACH1	PCH
PWR FAN	N/A	N/A	FANIO3	IT8720
			ICH_FAN_TACH2	PCH